

Fig 1

(PRIOR ART)



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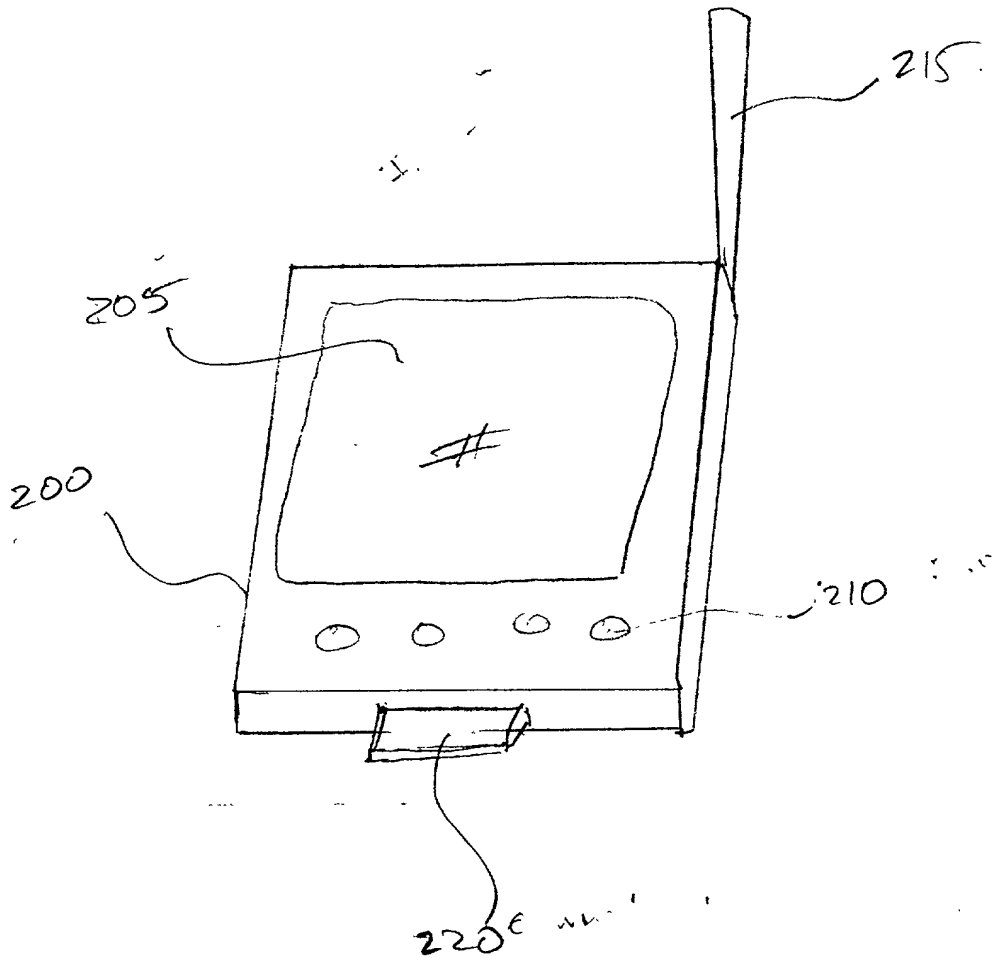


Fig 2  
(PRIOR ART)



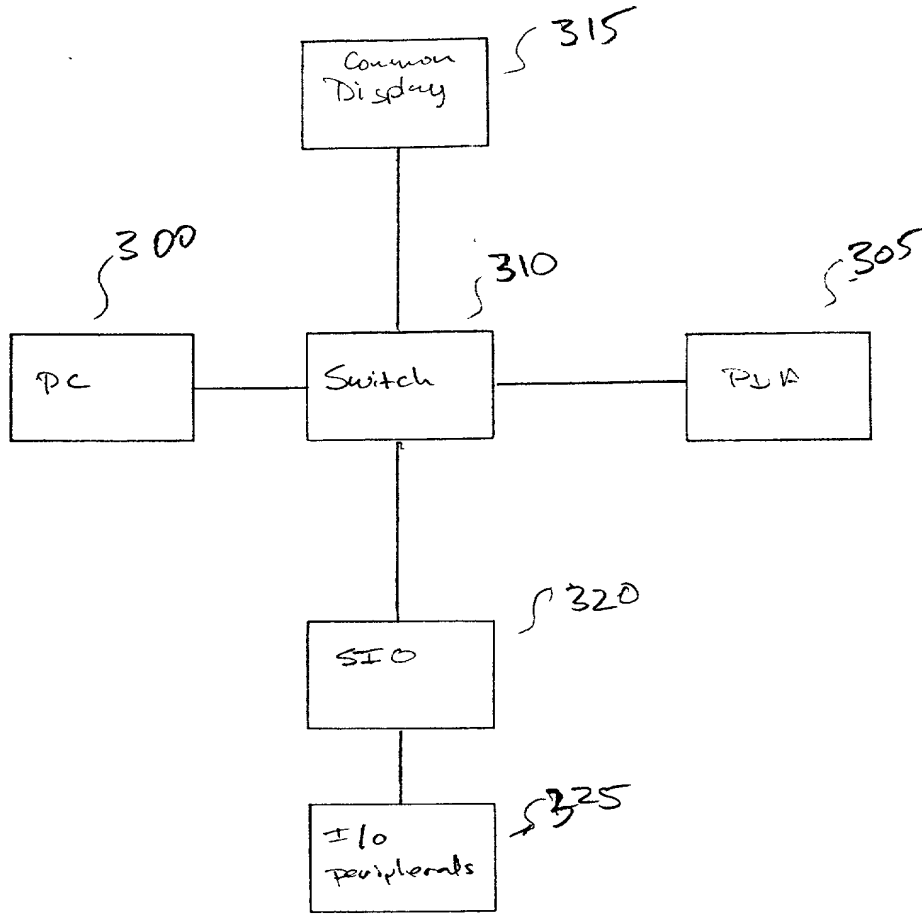


Fig. 3

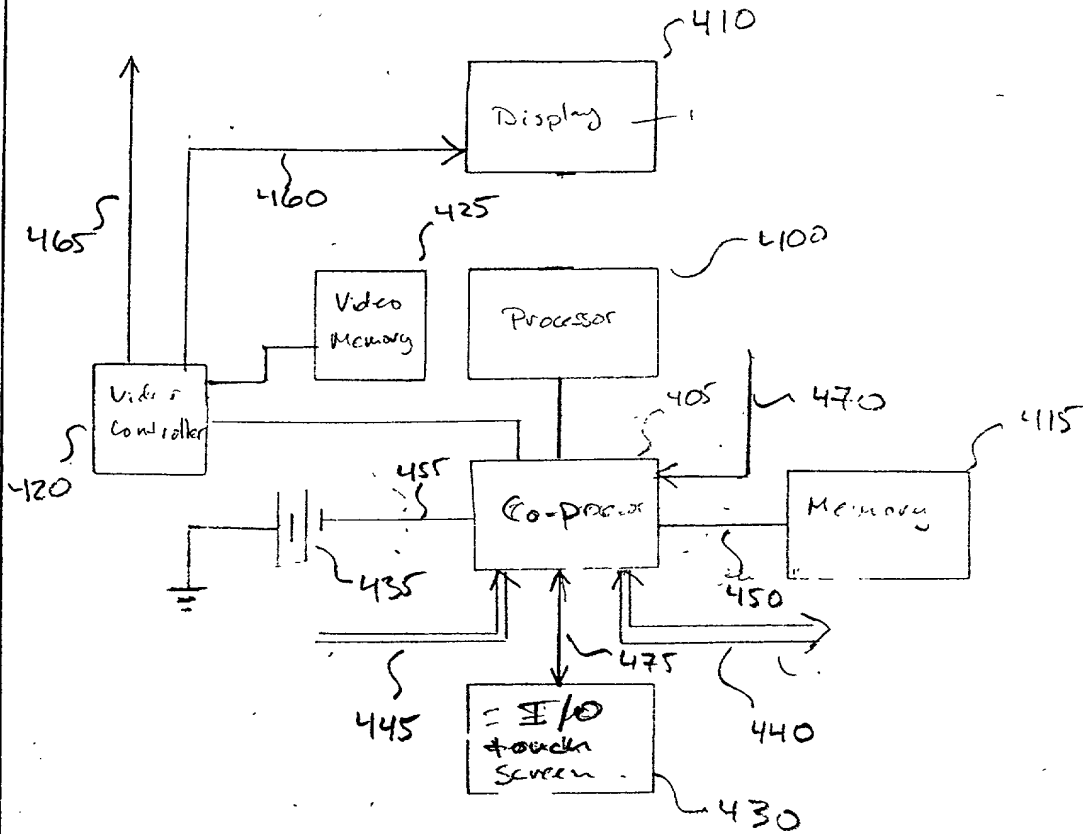
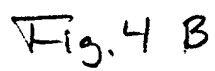


Fig. 4 A



# PDA to Chassis Connection

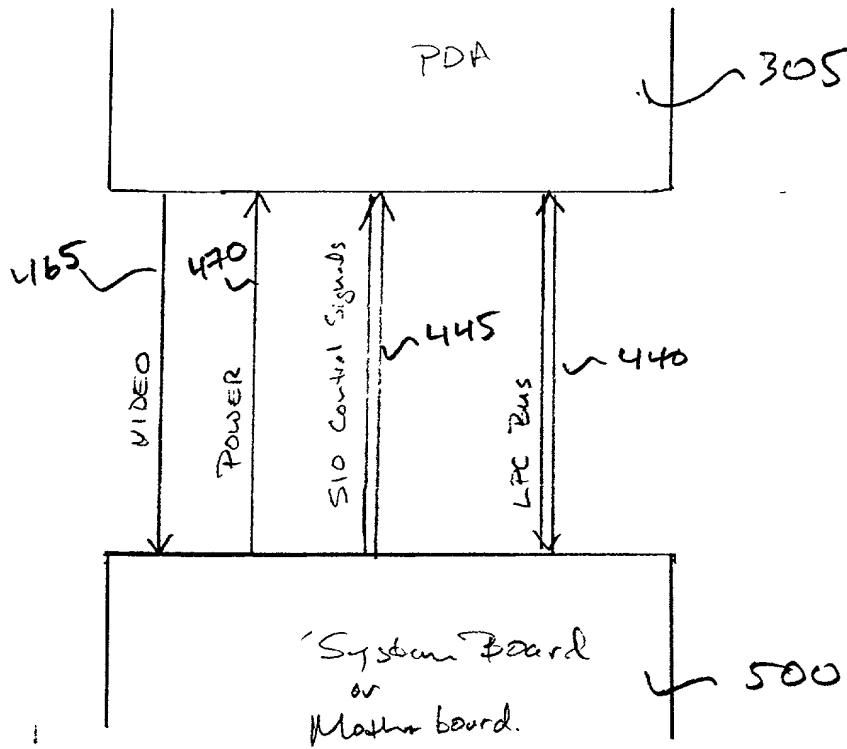


Fig 5



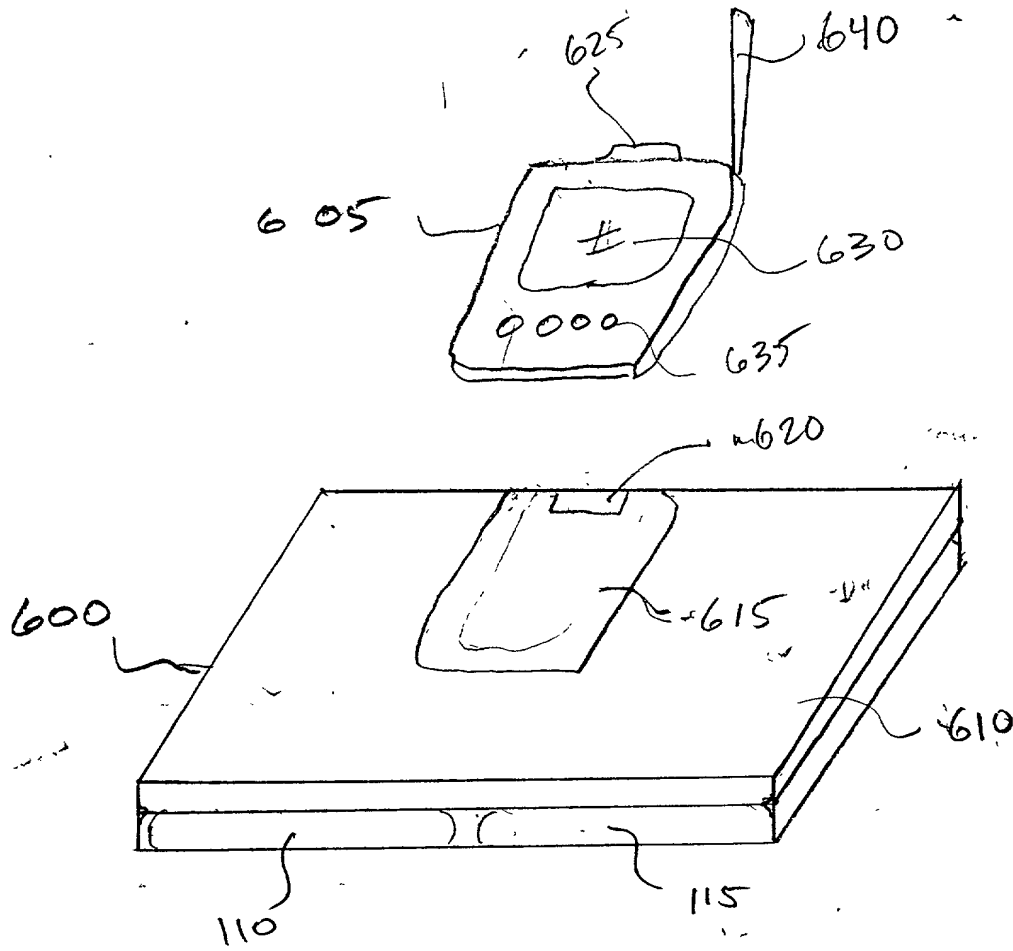


Fig. 6